

FIG. 1A

65053379

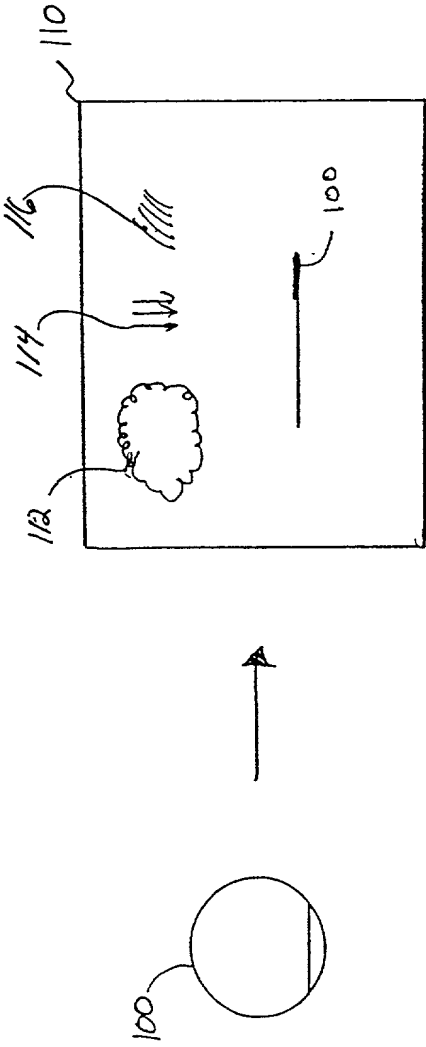


FIG. 1B



FIG. 1C

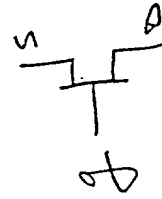


FIG. 1D



FIG. 1E

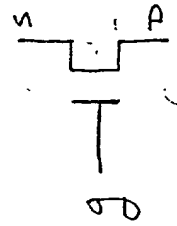


FIG. 1F

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FIG. 2A

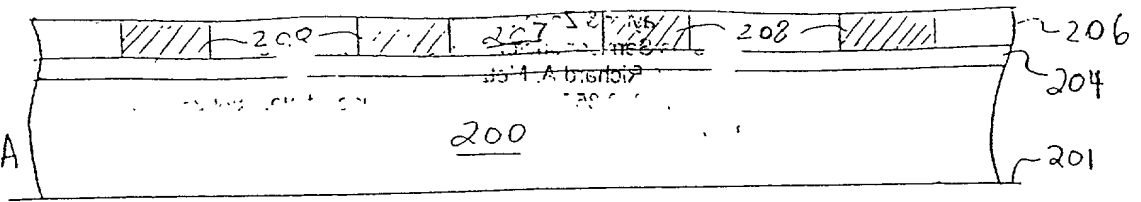


FIG. 2B

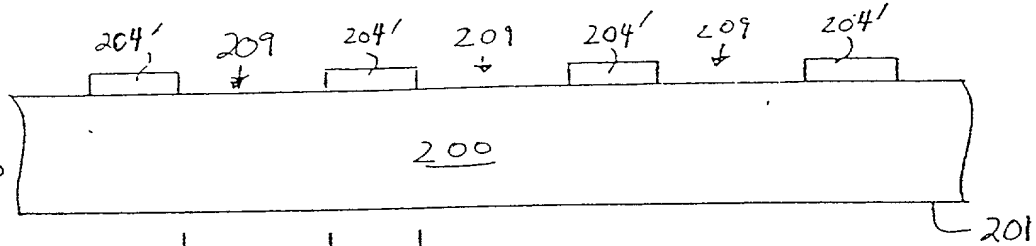


FIG. 2C

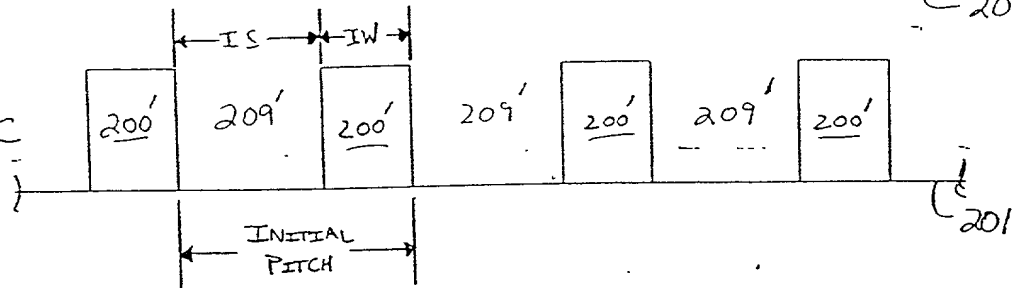


FIG. 2D

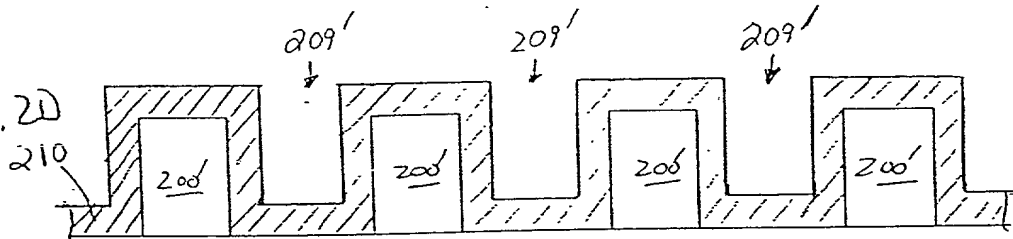


FIG. 2E

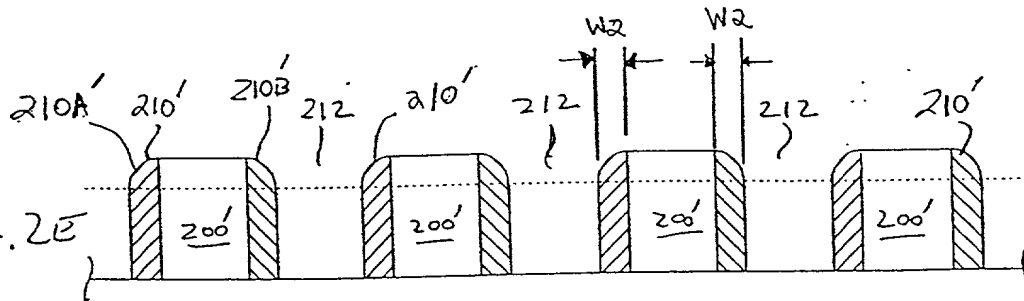
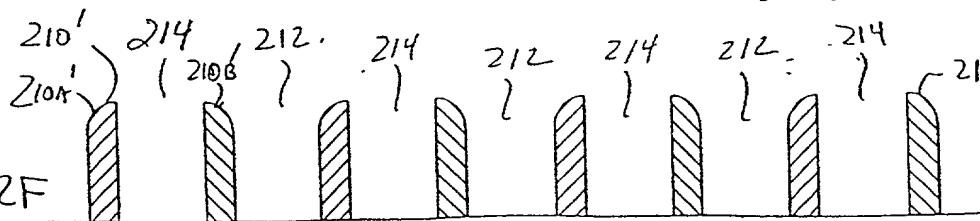


FIG. 2F



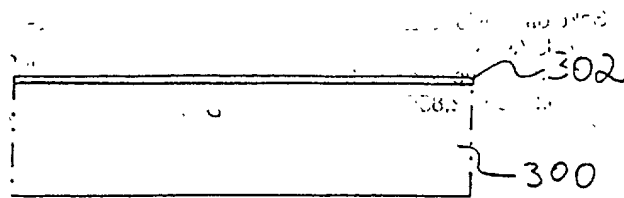


Fig. 3A

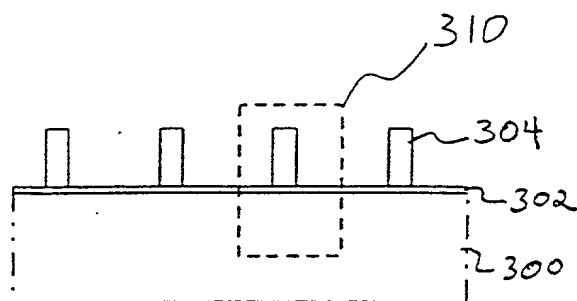


Fig. 3B

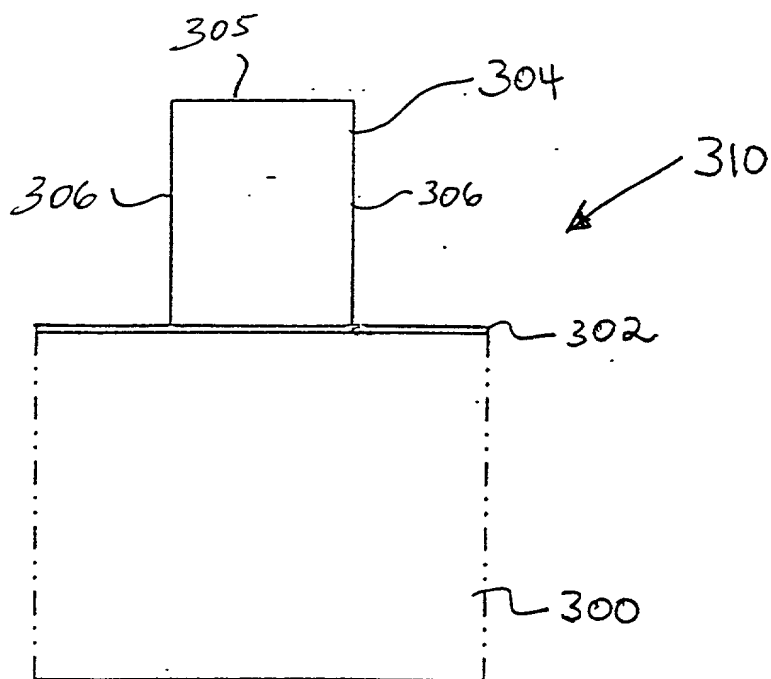


Fig. 3C

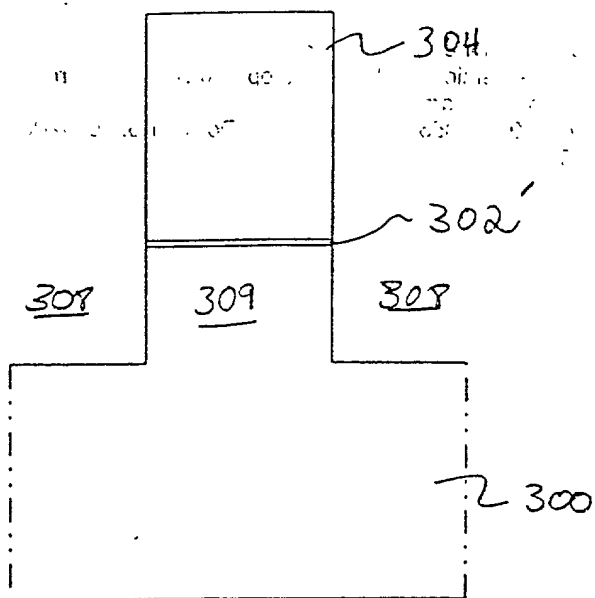


Fig. 3D

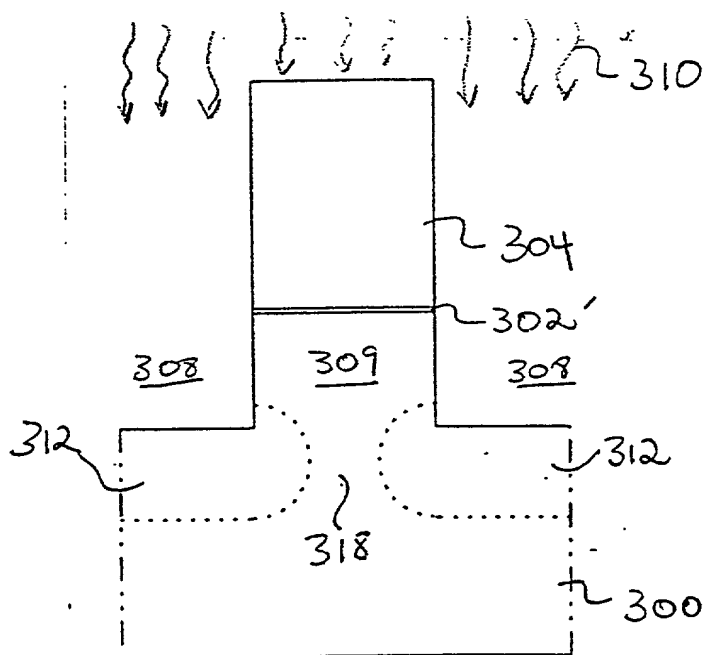


Fig. 3E

FIG. 3F is a cross-sectional view of the device 300 taken along line 3-3 of FIG. 3A, showing the device 300 in a second state. The device 300 includes a substrate 300, a first layer 302, a second layer 304, a third layer 306, a fourth layer 308, a fifth layer 310, a sixth layer 312, a seventh layer 314, an eighth layer 316, a ninth layer 318, and a tenth layer 320. The device 300 is shown in a second state, where the first layer 302 is in a first position and the second layer 304 is in a second position. The device 300 is shown in a second state, where the first layer 302 is in a first position and the second layer 304 is in a second position.

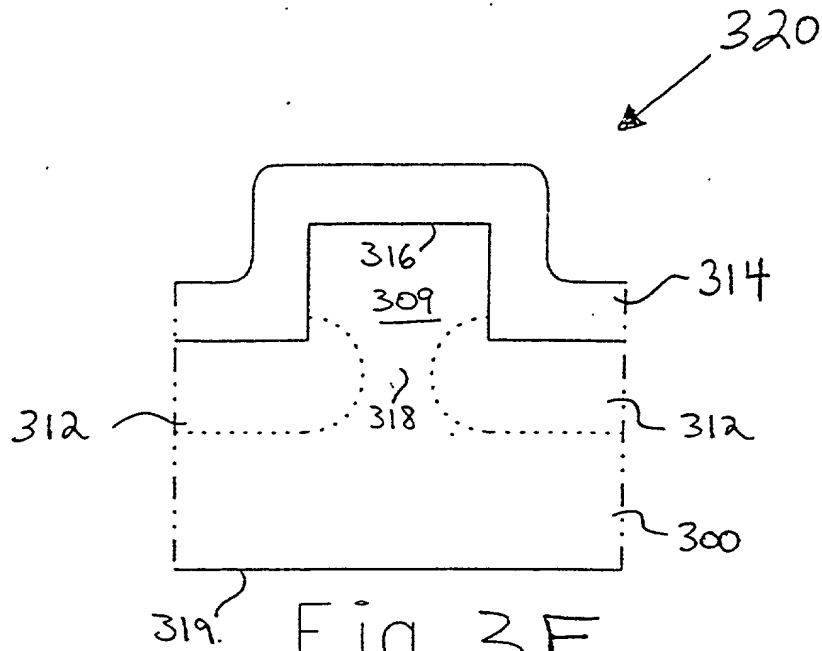


Fig. 3F

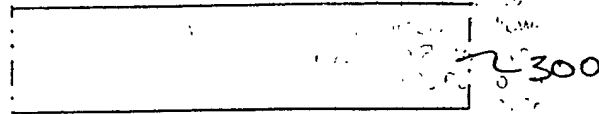


Fig. 4A

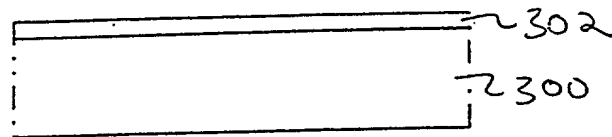


Fig. 4B

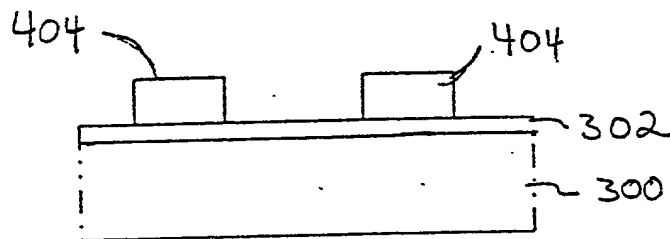


Fig. 4C

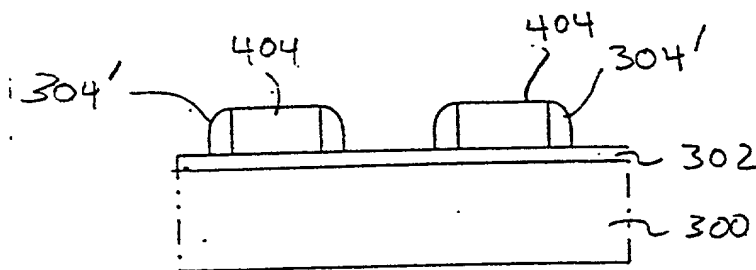
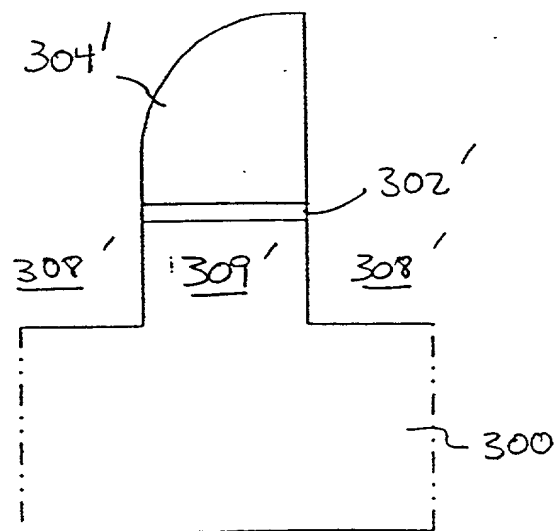
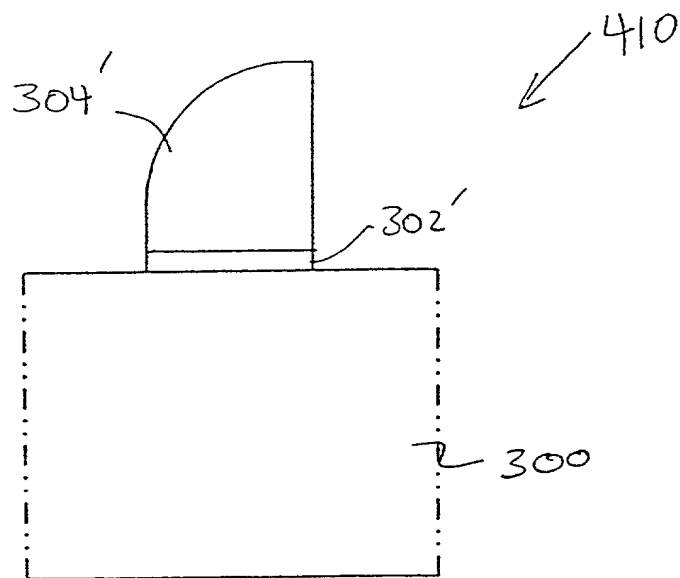
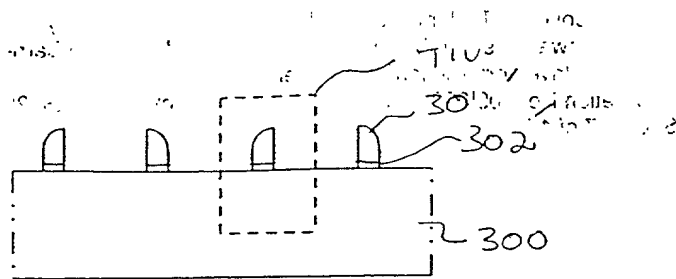
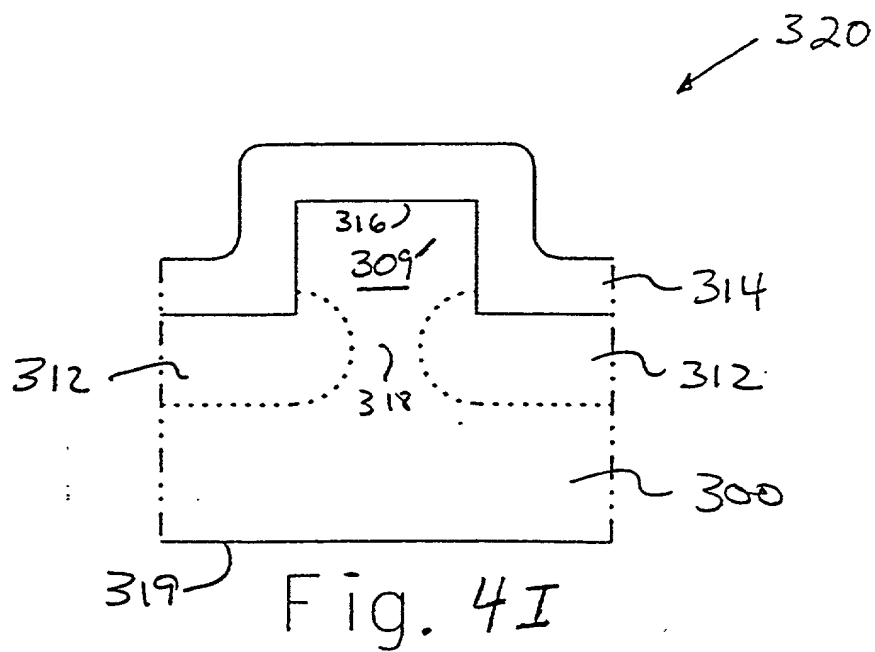
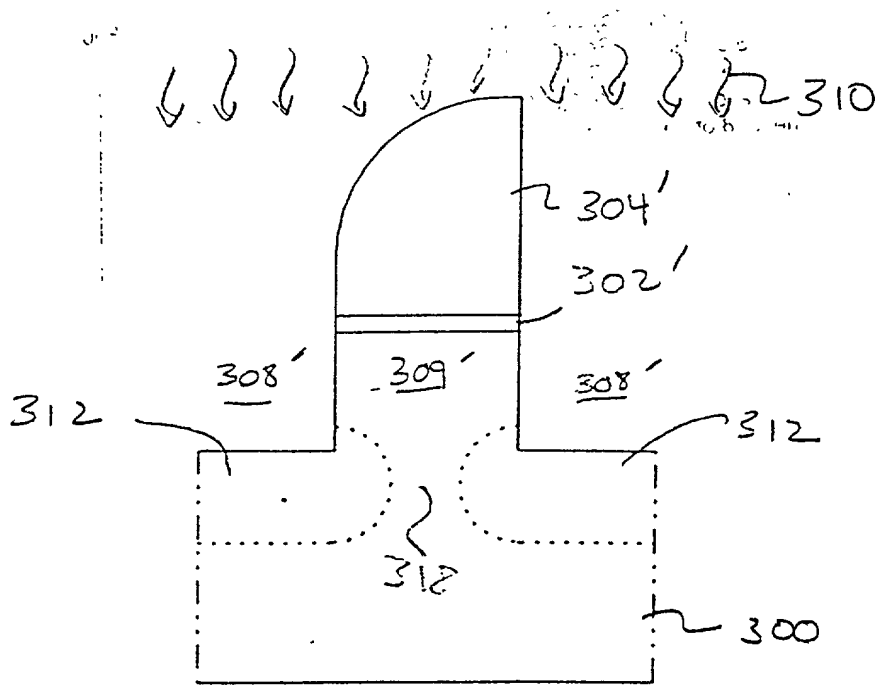


Fig. 4D

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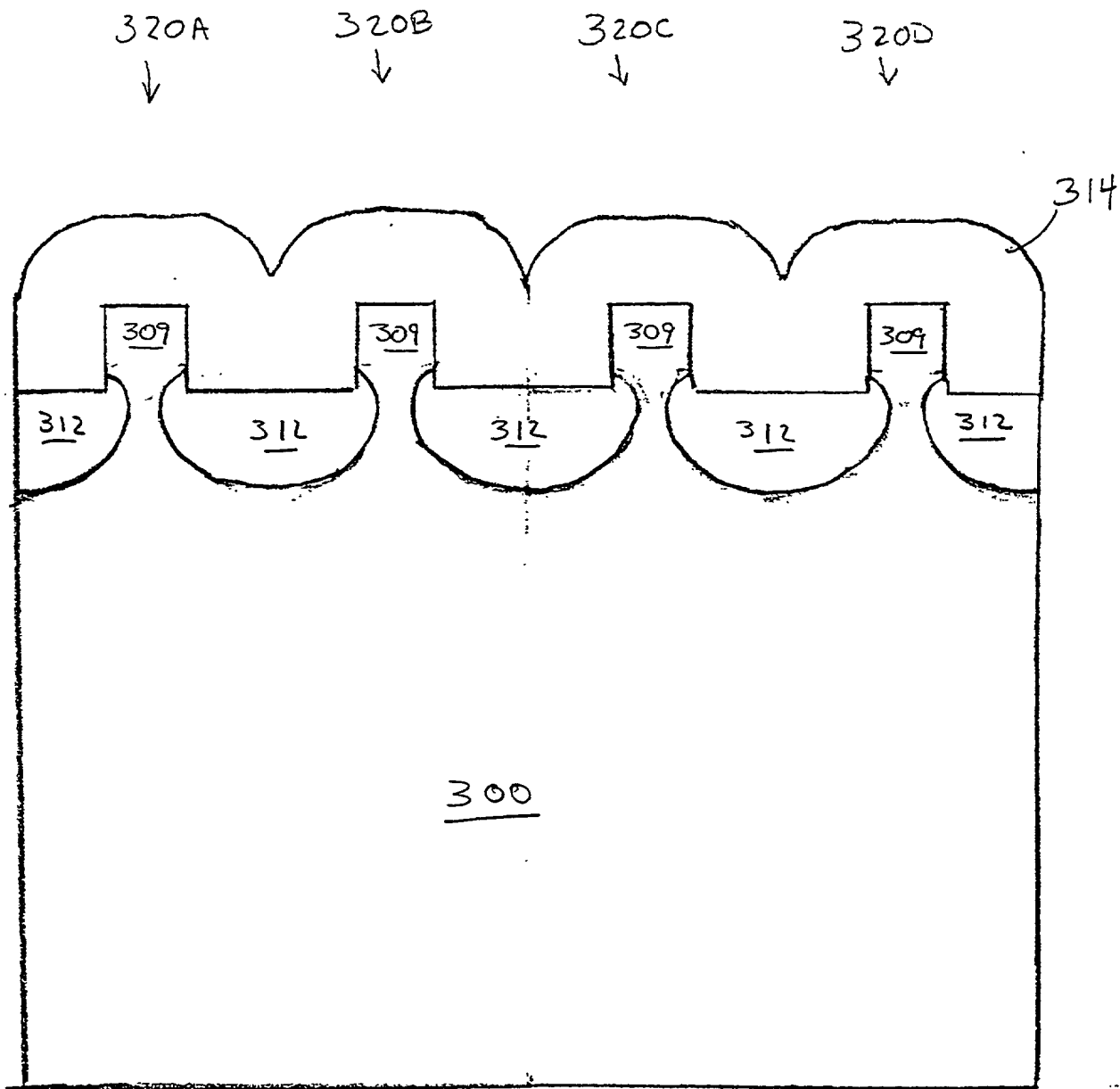


FIG. 5

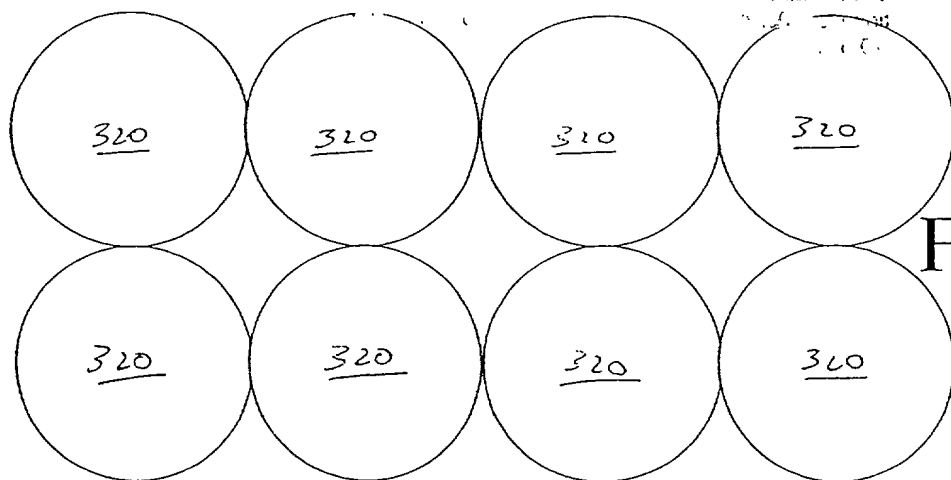


FIG. 6A

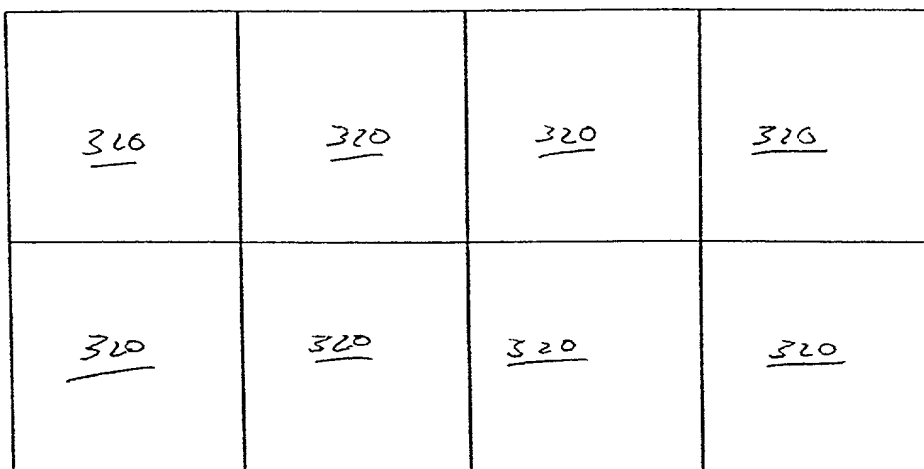


FIG. 6B

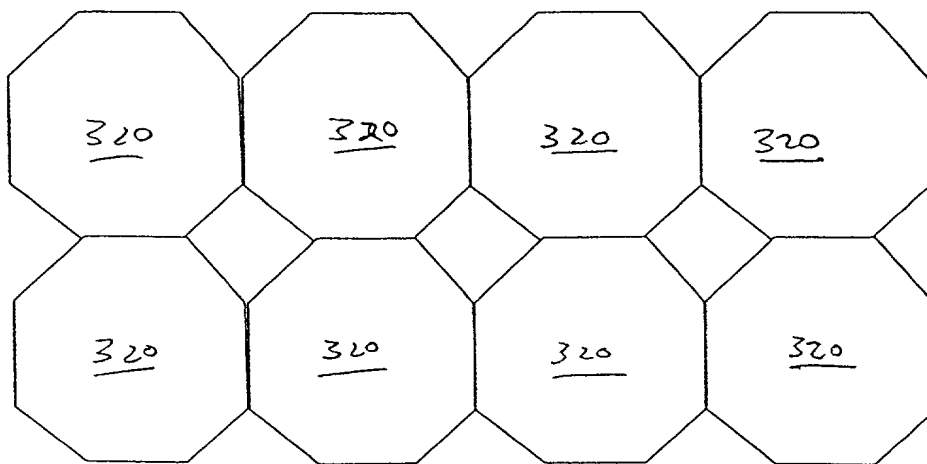


FIG. 6C

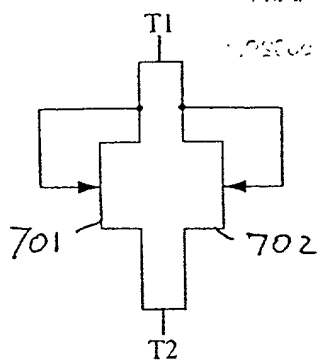


FIG. 7A

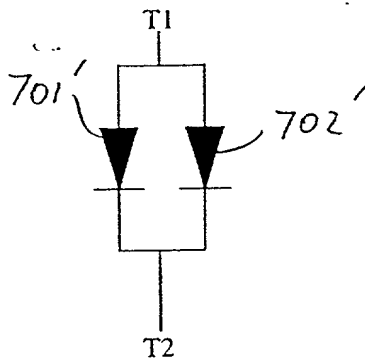


FIG. 7B

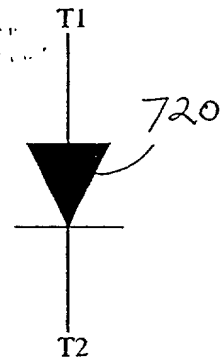


FIG. 7c

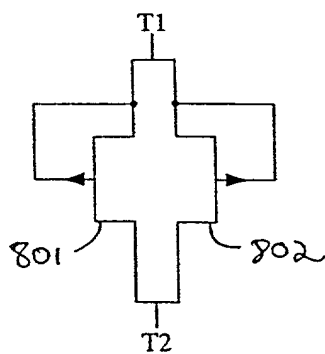


FIG. 8A

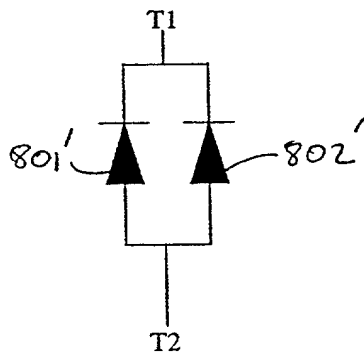


FIG. 8B

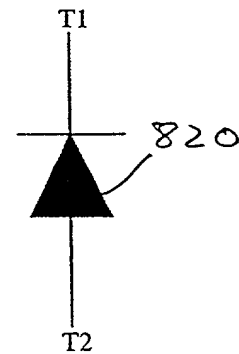


FIG. 8C

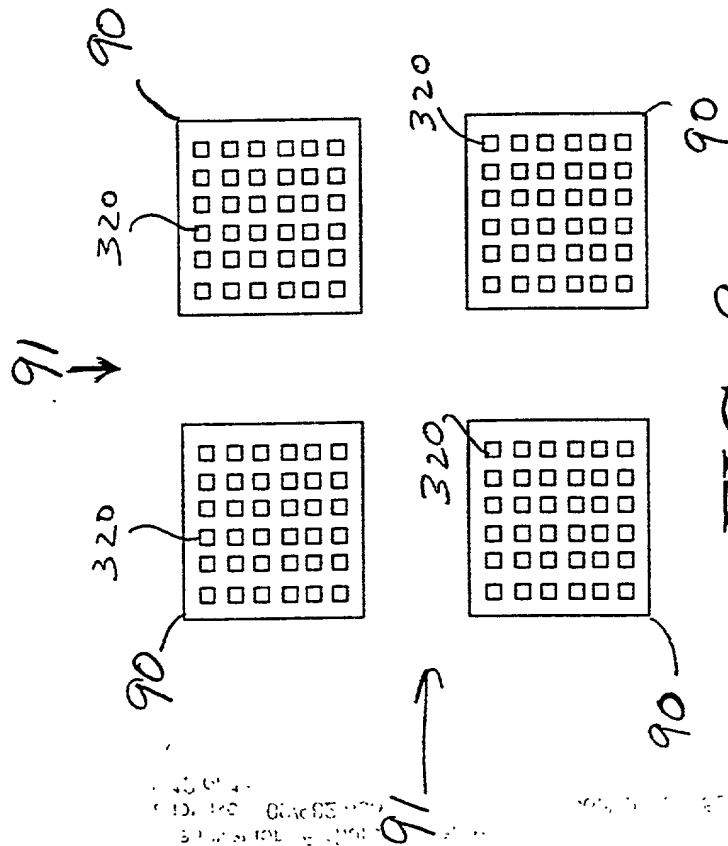


FIG. 9

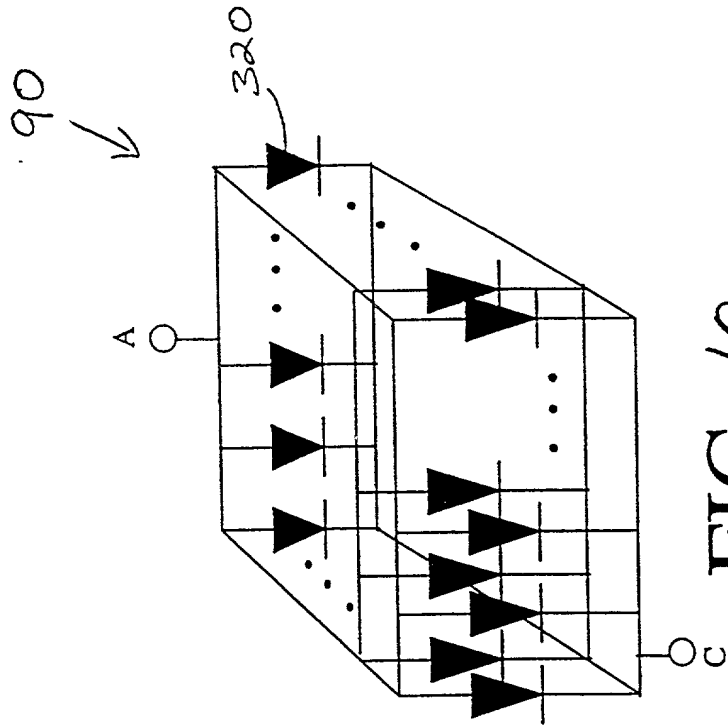


FIG. 10

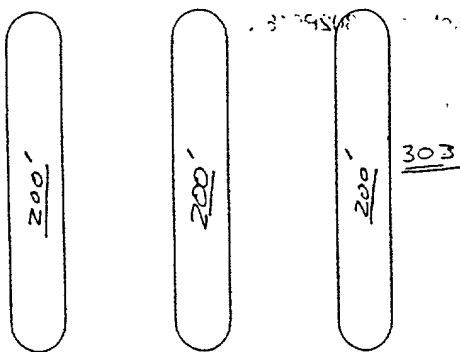


FIG. 11A

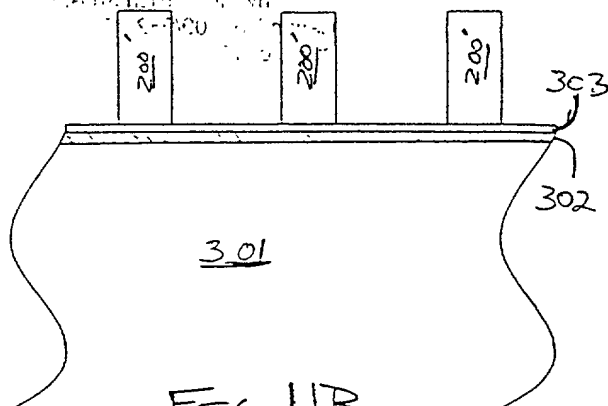


FIG. 11B

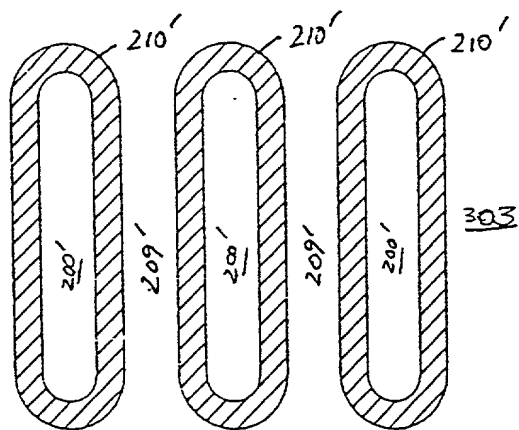


FIG. 12A

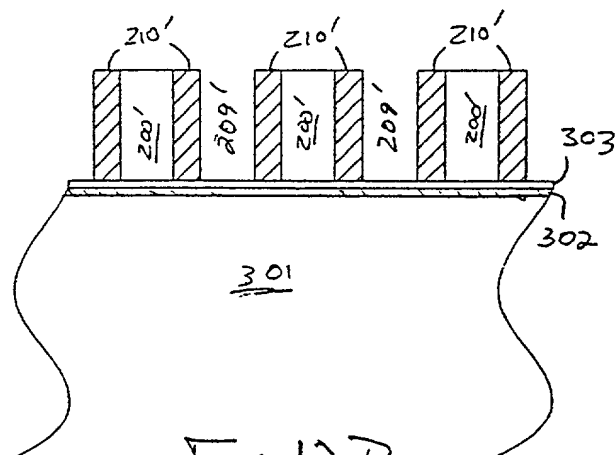


FIG. 12B

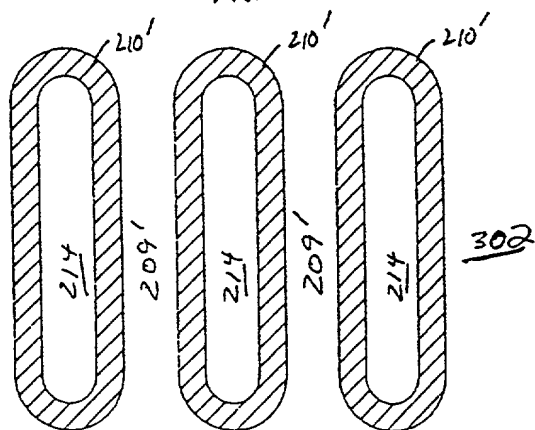


FIG. 13A

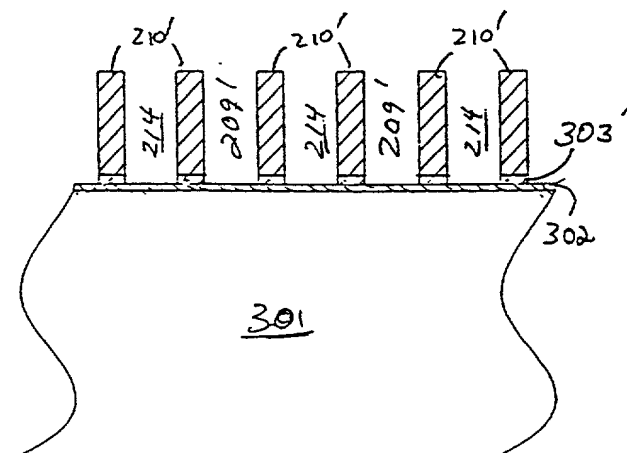


FIG. 13B

